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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/919,372	07/31/2001	Vadim Gutnik	5347-205	2525
20792 75	90 06/07/2006		EXAMINER	
MYERS BIGEL SIBLEY & SAJOVEC			CHUNG, PHUNG M	
PO BOX 37428 RALEIGH, NO			ART UNIT	
,			2138	
			DATE MAILED: 06/07/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)		
Office Action Summary		09/919,372	GUTNIK ET AL.		
		Examiner	Art Unit		
		Phung My Chung	2138		
Period fo	The MAILING DATE of this communication apports.	pears on the cover sheet with the c	orrespondence address		
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPL CHEVER IS LONGER, FROM THE MAILING D nsions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. Operiod for reply is specified above, the maximum statutory period to tre to reply within the set or extended period for reply will, by statute reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be time will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE!	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).		
Status					
	Responsive to communication(s) filed on <u>03 M</u> This action is FINAL . 2b) This Since this application is in condition for alloward closed in accordance with the practice under <u>B</u>	action is non-final. nce except for formal matters, pro			
Disposit	ion of Claims				
4)⊠ 5)□ 6)⊠ 7)⊠ 8)□ Applicati 9)□ 10)□	Claim(s) <u>1-26</u> is/are pending in the application 4a) Of the above claim(s) is/are withdraw Claim(s) is/are allowed. Claim(s) <u>1,7,8,14,19,25 and 26</u> is/are rejected	wn from consideration. ected to. r election requirement. er. epted or b) objected to by the Edrawing(s) be held in abeyance. See tion is required if the drawing(s) is objected.	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).		
	ınder 35 U.S.C. § 119				
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received.					
2) Notice 3) Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date	4) Interview Summary (Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:	(PTO-413) te atent Application (PTO-152)		

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 7-8, 14, 19 and 25-26 are rejected under 35 U.S.C. 102(b) as being anticipated by Llewellyn (5,329,251).

As per claims 1 and 7, Llewellyn discloses a clock distribution circuit, comprising:

A first clock circuit that is configured to generate a first clock signal responsive to an error signal;

A second clock circuit that is configured to generate a second clock signal responsive to the error signal; and

A phase detector circuit that connects the first clock circuit and the second clock circuit and is configured to generate the error signal responsive to the first and the second clock signals. (See col. 3, lines 29-33, lines 63-68).

As per claim 8, Llewellyn disclose a clock distribution circuit comprising:

A plurality of phase detector circuit;

A plurality of clock circuits..., the respective ones of the plurality of clock circuits being configured to generate respective ones of the plurality of clock signals responsive to respective ones of the plurality of error signals that are generated by the respective ones of the plurality of the phase detector circuits that directly connect the respective

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ones of the plurality of the clock circuits to the at least one other of the plurality of clock circuits. (See Fig. 3, col. 6, line 42 to col. 8, line 57).

As per claims 14, 19 and 25-26, these claims are rejected under similar rationale as set forth in claim 1.

- 3. Claims 2-6, 9-13, 15-18 and 20-24 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 4. Applicant's arguments with respect to claims 1-26 have been considered but are moot in view of the new ground(s) of rejection.
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phung My Chung whose telephone number is 571-272-3818. The examiner can normally be reached on Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 571-272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Phung My Chung

Primary Patent Examiner

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